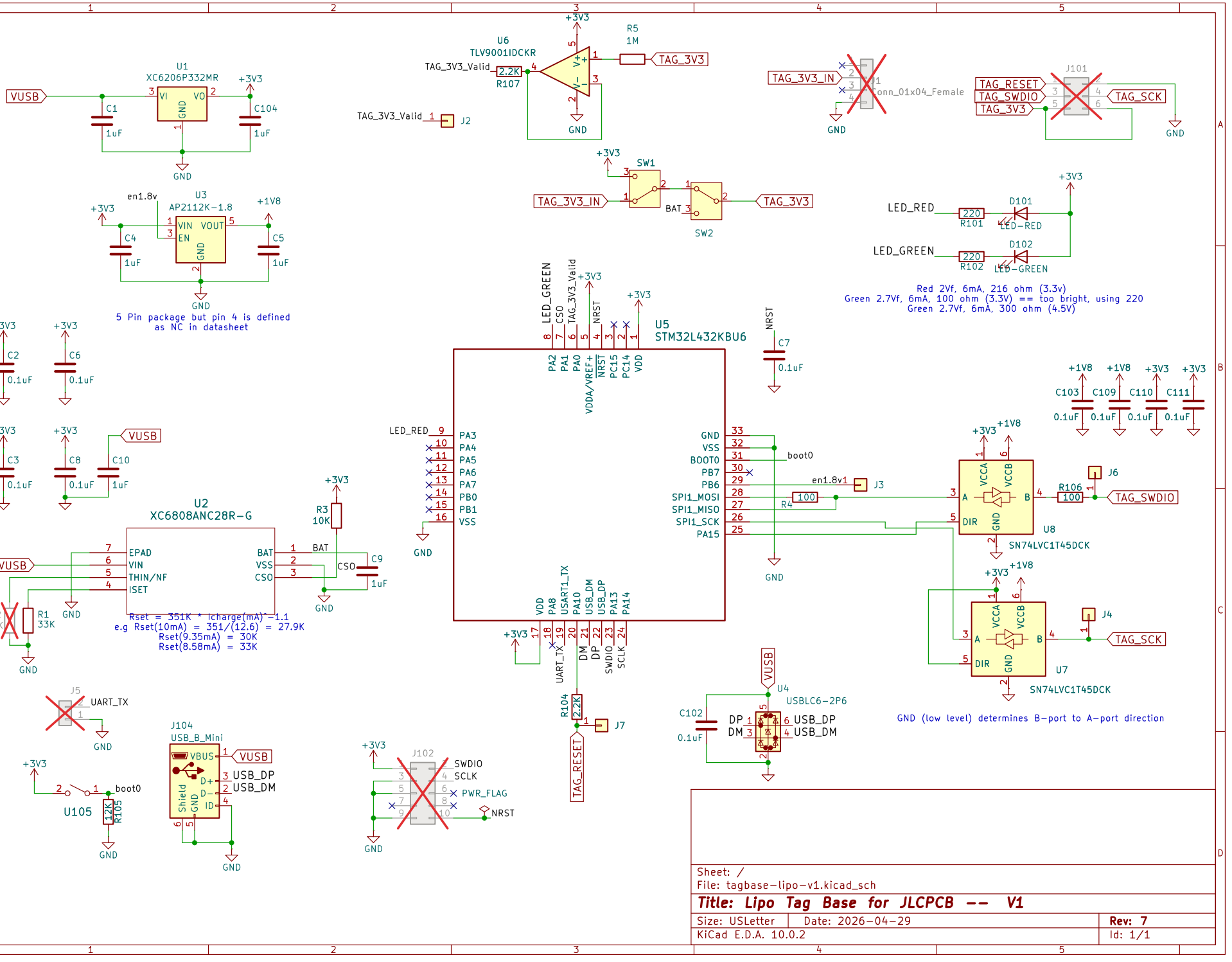

U1
XC6206P332MR
VUSB
+3V3
C1
1uF
C104
1uF
GND
U3
AP2112K-1.8
en1.8v
+1V8
C4
1uF
C5
1uF
5 Pin package but pin 4 is defined
as NC in datasheet
U6
TLV9001IDCKR
TAG_3V3_Valid
2.2K
R107
R5
1M
TAG_3V3
J2
TAG_3V3_IN
J1
Conn_01x04_Female
TAG_RESET
TAG_SWDIO
TAG_SCK
J101
SW1
SW2
BAT
LED_RED
LED_GREEN
220
R101
R102
D101
D102
LED-RED
LED-GREEN
Red 2Vf, 6mA, 216 ohm (3.3v)
Green 2.7Vf, 6mA, 100 ohm (3.3V) == too bright, using 220
Green 2.7Vf, 6mA, 300 ohm (4.5V)
C2
C6
C3
C8
C10
0.1uF
U2
XC6808ANC28R-G
EPAD
VIN
THIN/NF
ISET
BAT
VSS
CSO
R3
10K
C9
R1
33K
R2
10K
Rset = 351K * Icharge(mA)^-1.1
e.g Rset(10mA) = 351/(12.6) = 27.9K
Rset(9.35mA) = 30K
Rset(8.58mA) = 33K
U5
STM32L432KBU6
PA0
PA1
PA2
NRST
PC14
PC15
VDD
VDDA/VREF+
PA3
PA4
PA5
PA6
PA7
PB0
PB1
VSS
PB6
PB7
BOOT0
SPI1_MOSI
SPI1_MISO
SPI1_SCK
PA15
PA8
PA10
USART1_TX
USB_DM
USB_DP
PA13
PA14
SWDIO
SCLK
boot0
en1.8v1
J3
R4
100
C7
0.1uF
C103
C109
C110
C111
J6
R106
J4
J7
R104
2.2K
TAG_RESET
U8
SN74LVC1T45DCK
U7
GND (low level) determines B-port to A-port direction
U4
USBLC6-2P6
C102
DP
DM
J5
UART_TX
U105
12K
R105
J104
USB_B_Mini
VBUS
USB_DP
USB_DM
Shield
ID
J102
PWR_FLAG
Sheet: /
File: tagbase-lipo-v1.kicad_sch
Title: Lipo Tag Base for JLCPCB -- V1
Size: USLetter
Date: 2026-04-29
Rev: 7
KiCad E.D.A. 10.0.2
Id: 1/1